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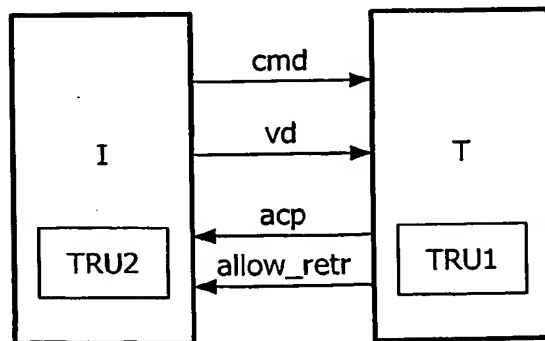
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(54) Title: INTEGRATED CIRCUIT AND METHOD FOR TRANSACTION RETRACTION



(57) Abstract: An integrated circuit having a plurality of  
processing modules (I, T) is provided. At least one first  
processing module (I) issues at least one transaction to-  
wards at least one second processing module (T). Said in-  
tegrated circuit further comprises at least one first trans-  
action retraction unit (TRU1) for indicating an allowance  
to said at least one first of said processing modules (I) to  
retract said at least one transaction according to the state of  
said second processing module (T).

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